

Microelectronics Journal 37 (2006) 295-301

Microelectronics Journal

www.elsevier.com/locate/mejo

Chemical mechanical polishing of polymeric materials for MEMS applications

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> Received 23 March 2005; received in revised form 9 May 2005; accepted 16 May 2005 Available online 1 July 2005

Abstract

Polymeric materials such as polycarbonate (PC) and poly-methyl methacryate (PMMA) are replacing silicon as the major substrate in microfluidic system fabrication due to their outstanding features such as low cost and good chemical resistance. In this study, chemical mechanical polishing (CMP) of PC and PMMA substrates was investigated. Four types of slurry were tested on CMP of the polymers under the same process conditions. The slurry suitable for polishing PC and PMMA was then chosen, and further CMP experiments were carried out under different process conditions. Experimental results showed that increasing table speed or head load increased the material removal rates of the polymers. The polymeric surface quality after CMP was acceptable to most MEMS applications. Analysis of variance was also carried out, and it was found that the interaction of head load and table speed had a significant (95% confidence) effect on surface finish of polished PMMA. On the other hand, table speed had a highly significant (99% confidence) effect on surface finish of polished PC. © 2005 Elsevier Ltd. All rights reserved.

Keywords: Polycarbonate; Poly-methyl methacryate; Chemical mechanical polishing; Material removal rate; Surface roughness; Analysis of variance

1. Introduction

In an article entitled 'Silicon as a micromechanical material', Petersen [1] described techniques for microelectro-mechanical systems (MEMS) fabrication. Processing of silicon wafers has played an enhanced role in manufacturing of MEMS [2]. On the other hand, increasing demands for high electrical performance have led to significant advances in integrated circuit (IC) fabrication and microelectronics packaging [3–5].

Technical advances inspired the development of chemical mechanical polishing (CMP) for IC fabrication at IBM [6,7]. As semiconductor chips are highly integrated, more precise planarization of each layer on chips is needed [8]. Interlevel dielectric planarization by CMP is necessary for technologies beyond the 0.35-µm CMOS (complimentary metal-oxide semiconductor) generation [9]. CMP has emerged as the preferred manufacturing process for

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planarizing multi-level metal layers in ICs [10,11], although diamond grinding can also generate mirror surfaces on brittle materials including silicon [12–17].

A dielectric constant k < 3, gap filling at high aspect ratios [18], and global planarization are challenging demands for dielectrics in conventional multilevel metallization schemes of modern IC technologies [19]. Novel IC devices based on structures of low-*k* material/Cu have been proposed recently using the CMP technology [20]. However, reduction of scratches is required in advanced CMP processes [21]. Ultra-low-*k* dielectrics are prone to damage during CMP. Most ultra-low-*k* materials are also attacked by CMP or post-CMP cleaning chemicals [22]. Other CMP applications include polishing of Ba_{0.6}Sr_{0.4}TiO₃ for ferroelectric random access memory applications [23] and SiLK dielectric planarization [24].

For MEMS applications, CMP processes have also been developed for planarizing silicon dioxide and metal layers [25]. CMP is used to achieve flat, mirror-like surfaces for optical devices, planarize the topology from previous manufacturing steps, and reduce film roughness for more precise lithography or for wafer bonding [26].

Traditionally, silicon and glass have been the major materials used in MEMS [27]. However, many biomedical

devices require soft or polymeric materials such as polycarbonate (PC) and polyimide. Polymers are inexpensive and can be used in disposable devices, relaxing stringent sterilization-on-reuse requirements. They provide a more suitable interface with biological tissue [28]. Soft materials have many attributes that make them ideally suited for defining microfluidic, optical and nanoelectromechanical structures with low-cost replication processes [29].

Therefore, further developments have focused on substituting silicon and silicon dioxide with polymers in recent years. For semiconductor devices, polymers excel due to their relatively low dielectric constants, minimized interconnection delays and improved conductor-packaging density. In MEMS, the use of polymers is mostly aimed at creating high-aspect-ratio structures [8].

Microfluidics [30,31], a MEMS technology, enables the fabrication of networks of channels, chambers, and valves to control the flow of liquids in amounts as minute as one picoliter. Chemical analysis, drug delivery, biological sensing and environmental monitoring typically incorporate MEMS microfluidic devices [32].

Recent developments in MEMS fabrication techniques have moved away from the mask/etch paradigm and instead exploit properties of polymers. Imprinting techniques can be used to fabricate microfluidic devices on poly-methyl methacryate (PMMA) substrates [33]. When choosing a polymer-based substrate, its properties are critical for the fabrication process and successful application of the device [34]. Because of their properties like fine resolution and stability, PMMA and PC are also common materials for many other applications [35,36].

In a CMP process, one polishing condition can result in a large difference in material removal rates (MRRs), which influence other important aspects of a CMP process, such as planarization efficiency [37]. Major process parameters of CMP are slurry, polishing pressure, polishing pad material, polishing pad and workpiece spinning speeds. The success of CMP depends on selection of process parameters [38].

Common problems encountered in using CMP arise from lack of predictive capability regarding the MRR and its dependence on various parameters, and difficulties in removal of colloidal particles adhering to the workpiece surface after polishing [30]. The demand of lithographic exposure resolution increases when component sizes decrease. The global planarization technology becomes increasingly important, although CMP may also introduce surface defects and increase the demand for automatic defect-detection techniques [39]. CMP mechanisms are very complicated and not understood clearly yet. It is extremely difficult to analyze its polishing mechanisms [40,41]. Therefore, experiments are conducted repeatedly to ensure the best process conditions [42].

Reports on CMP of polymeric materials are still scarce, compared to reports on CMP of silicon wafers. In this study, CMP of PC and PMMA substrates was investigated. Four types of slurry were tested on CMP of these polymers under

Table 1						
Parameters	and their	settings	for the	CMP	experiment	ts

Parameter	Setting (experiment 1)	Setting (experiment 2)
Process time (min)	1	1
Table speed (rpm)	30	20, 30, 40
Oscilation speed (mm/sec)	2	2
Spindle speed (rpm)	40	40
Head load (g/cm ²)	100	75, 100, 125
Slurry flow rate (ml/min)	100	100
Slurry	Simlox, ILD 1200,	The slurry selected
	Mazin SRS1 and SRS3	after experiment 1

the same process conditions. The slurry suitable for polishing PC and PMMA was then chosen, and further CMP experiments were carried out under different process conditions to study the effects of two key process parameters on CMP of PC and PMMA.

2. Experiments

PMMA and PC samples were prepared by cutting them into 2-mm-thick round-shape plates with a 150-mm diameter. A face polisher was used to grind surfaces of the samples under a MD 600 polishing disc. The surface roughness after grinding was between 0.1 and 0.3 μ m. The samples were then polished using specified parameters on an Okamoto SPP-600S CMP machine.

Table 1 shows the parameter settings used. The slurry flow rate was 100 ml/min, the spindle rotation speed was 40 rpm, and the oscillation speed was 2 mm/s.

In experiment 1, head load was 100 g/cm² and table speed was 30 rpm, which were typical polishing conditions for bulk removal of silicon using the CMP machine. Four types of slurry, namely Simlox, ILD 1200, Mazin SRS1 and SRS3, were evaluated and the most suitable slurry for CMP of the polymers was then selected based on analysis of MRRs and surface finish of polished samples. ILD 1200 is



Fig. 1. MRRs of the polymers polished using the four types of slurry.



Fig. 2. Results of the material hardness test.



Fig. 3. Surface roughness of the polymers after CMP using four types of slurry.

fumed silica polishing slurry containing ammonium hydroxide. Mazin SRS1 and SRS3 are colloidal silica polishing slurry for stock removal applications. Okamoto Simlox A1136 is slurry designed for removal of polymeric materials. In experiment 2, two key process parameters, head load and table speed, were varied to examine their effects on polishing efficiency and quality. To limit the number of experiments, three levels (low, median and high) for each parameter were assigned: 75, 100 and 125 g/cm² for head load, and 20, 30 and 40 rpm for table speed. The slurry selected after experiment 1 was used.

Before and after polishing, the thickness of samples was measured using a head thickness gauge to compute the MRR, which was defined to be the thickness reduction per minute in this study. After CMP, surface roughness of samples was measured using an atomic force microscope (AFM), and the arithmetic average roughness R_a and the root-mean-square roughness R_q were obtained. AFMs are widely used for characterization of smooth surfaces [43] and inspection of surface deformations [44].

3. Results and discussion

Fig. 1 shows measured MRRs of the polymers polished using the four types of slurry. MRRs of PMMA are between 0.009 and 0.018 μ m/min, which are much lower than those of PC (0.043–0.064 μ m/min). This significant difference results from the difference of material hardness. Material hardness of the polymers was measured using a Vickers hardness tester and the results are shown in Fig. 2. The average hardness of PMMA and PC substrates used in our experiments was 161 and 134 MPa, respectively. PMMA is harder than PC. The MRRs of PC were seven and three times those of PMMA when ILD 1200 and Simlox were used, respectively.

The measured surface roughness of the polymers polished using the four types of slurry is shown in Fig. 3. PMMA samples are harder and after CMP have better surface finish (lower R_a values) than PC samples polished



Fig. 4. Effects of head load and table speed on MRRs of PMMA.



Fig. 5. Effects of head load and table speed on MRRs of PC.

under the conditions used. The smoothest PMMA and PC surfaces were obtained by CMP using Simlox.

Figs. 1 and 3 do not indicate which slurry is the most effective in polishing both polymers and at the same time can produce the best surface finish. However, Simlox produced relatively high MRRs and the lowest surface roughness values in polishing the polymers. Therefore, it was selected to be the CMP slurry, and was used in experiment 2 to evaluate effects of two key process parameters.

Figs. 4 and 5 show the effects of head load and table speed on MRRs in polishing of PMMA and PC. MRRs increase with increased head load and table speed. The results approximately agree with the Preston Equation [1,6]:

$$MRR = K_p P \frac{\Delta s}{\Delta t} \tag{1}$$

Where *P* is the pressure, $\Delta s/\Delta t$ is the linear velocity of the pad relative to the workpiece and K_p is the Preston

coefficient. Another trend revealed from the two figures was that within the chosen experimental parameter ranges, the variation of table speed introduced a more significant change in MRRs than that of head load.

Surface roughness of polished PMMA and PC samples is shown in Figs. 6 and 7, respectively. Figs. 8 and 9 show examples of AFM images of polished polymers. These figures show that the softer polymer (PC) after CMP had deeper micro scratches than the harder polymer (PMMA) after CMP, and therefore had higher surface roughness values. However, all of polished PMMA and PC surfaces had nanometer-order surface roughness heights. In general, the surface quality after CMP appeared to be acceptable to most MEMS applications. However, intensive post-CMP cleaning was required to get rid of particles and chemical remains introduced by the CMP process.

Directly from Figs. 6 and 7, it is difficult to draw any conclusions whether there are any significant effects of head load and table speed on surface roughness of polished



Fig. 6. Effects of head load and table speed on surface finish of polished PMMA.



Fig. 7. Effects of head load and table speed on surface finish of polished PC.



Fig. 8. AFM image of PMMA polished using Simlox, table speed = 20 rpm, and head load = 75 g/cm².



Fig. 9. AFM image of PC polished using Simlox, table speed = 20 rpm, and head load = 125 g/cm².

PMMA and PC. Therefore, analysis of variance (ANOVA) of the two-factor factorial experiments was carried out using the measured surface roughness values to investigate the effects on surface finish of polished PMMA and PC, as shown in Tables 2 and 3, respectively. The summary of the ANOVA results is shown in Table 4.

It was found that only the interaction of head load and table speed had a significant (risk $\alpha = 0.05$, 95% confidence) effect, and individual head load and table speed had no significant effects on surface finish of polished PMMA samples. On the other hand, only table speed had a highly significant ($\alpha = 0.01$, 99% confidence) effect, and individual head load and the interaction of head load and table speed

Table 2			
ANOVA	table	for	PMMA

Source of variation	Sum of squares	Degrees of freedom	Mean square	F value
Table speed	0.498	2	0.249	0.346
Head load	0.68	2	0.34	0.472
Interaction of head load and table speed	12.56	4	3.14	4.361
Error Total	6.48 20.184	9 17	0.72	

Table 3 ANOVA table for PC

Source of variation	Sum of squares	Degrees of freedom	Mean square	F value
Table speed	22.788	2	11.394	8.421
Head load	2.354	2	1.177	0.870
Interaction of	10.232	4	2.558	1.891
head load and				
table speed				
Error	12.176	9	1.353	
Total	47.55	17		

Table 4

Summary of ANOVA results

Level of effect	Factor	Material	
		PMMA	PC
Significant	Table speed	No	Yes
$(\alpha = 0.05)$	Head load	No	No
	Interaction	Yes	No
Highly significant	Table speed	No	Yes
$(\alpha = 0.01)$	Head load	No	No
	Interaction	No	No

had no significant effects on surface finish of polished PC samples.

4. Conclusions

Four types of slurry were used as abrasives and experiments of CMP of PMMA and PC were conducted under the same process conditions. The MRRs and surface roughness of PC were found to be much higher than those of PMMA due to different material hardness, regardless of the types of slurry used. Material hardness of PMMA (161 MPa) is higher than that of PC (134 MPa). Simlox slurry was found to be suitable for CMP of the two polymers. Then, CMP of PMMA and PC was performed by varying two key process parameters, namely table speed and head load while other parameters were kept constant. The MRRs were significantly affected by table speed and head load. Increasing table speed or head load will increase MRRs. All polished PMMA and PC surfaces had nanometer-order surface roughness heights, acceptable to most MEMS applications. ANOVA was also carried out, and it was found that the interaction of head load and table speed had a significant (risk $\alpha = 0.05$, 95% confidence) effect on surface finish of polished PMMA samples. On the other hand, table speed had a highly significant ($\alpha = 0.01$, 99% confidence) effect on surface finish of polished PC samples.

Acknowledgements

The authors thank Ms Liu Yuchan of SIMTech, Mr. Zirajutheen B.M.P. and Mr. Tan Y.S. of Nanyang Technological University for their assistance.

References

- K.E. Petersen, Silicon as a mechanical material, Proceedings of IEEE 70 (5) (1982) 420–457.
- [2] Z.W. Zhong, W.H. Tok, Grinding of single-crystal silicon along crystallographic directions, Materials and Manufacturing Processes 18 (5) (2003) 811–824.
- [3] Z. Zhong, Reliability of FCOB with and without encapsulation, Soldering & Surface Mount Technology 13 (2) (2001) 21–25.
- [4] Z. Zhong, P.K. Yip, Finite element analysis of a three dimensional package, Soldering & Surface Mount Technology 15 (1) (2003) 21–25.
- [5] Z.W. Zhong, K.W. Wong, X.Q. Shi, Interfacial behaviour of a flip chip structure under thermal testing, IEEE Transactions on Electronics Packaging Manufacturing 27 (1) (2004) 43–48.
- [6] J.M. Steigerwald, S.P. Murarka, R.J. Gutmann, Chemical Mechanical Planarization of Microelectronic Materials, Wiley, New York, 1997.
- [7] Y. Liu, K. Zhang, F. Wang, W. Di, Investigation on the final polishing slurry and technique of silicon substrate in ULSI, Microelectronic Engineering 66 (1–4) (2003) 438–444.
- [8] W. Cho, Y. Ahn, C.-W. Baek, Y.-K. Kim, Effect of mechanical process parameters on chemical mechanical polishing of Al thin films, Microelectronic Engineering 65 (1–2) (2003) 13–23.
- [9] P. van der Velden, Chemical mechanical polishing with fixed abrasives using different subpads to optimize wafer uniformity, Microelectronic Engineering 50 (1–4) (2000) 41–46.
- [10] C. Kourouklis, T. Kohlmeier, H.H. Gatzen, The application of chemical-mechanical polishing for planarizing a SU-8/permalloy combination used in MEMS devices, Sensors and Actuators A 106 (2003) 263–266.
- [11] C.L. Lai, S.H. Lin, Electrocoagulation of chemical mechanical polishing (CMP) wastewater from semiconductor fabrication, Chemical Engineering Journal 95 (2003) 205–211.
- [12] Z. Zhong, New grinding methods for aspheric mirrors with large curvature radii, Annals of CIRP 41/1 (1992) 335–338.
- [13] Z. Zhong, V.C. Venkatesh, Generation of parabolic and toroidal surfaces on silicon and silicon based compounds using diamond cup grinding wheels, Annals of CIRP 43/1 (1994) 323–326.
- [14] Z. Zhong, W.Y. Lee, Grinding of silicon and glass using a new dressing device and an improved coolant system, Materials and Manufacturing Processes 16 (4) (2001) 471–482.
- [15] S. Chidambaram, Z.J. Pei, S. Kassir, Fine grinding of silicon wafers: a mathematical model for grinding marks, Machine Tools & Manufacture 43 (2003) 1595–1602.
- [16] Z.W. Zhong, Ductile or partial ductile mode machining of brittle materials, The International Journal of Advanced Manufacturing Technology 21 (8) (2003) 579–585.
- [17] Z.W. Zhong, H.B. Yang, Development of a vibration device for grinding with microvibration, Materials and Manufacturing Processes 19 (6) (2004) 1121–1132.
- [18] Z.W. Zhong, T.M. Lye, Effects of profiles and conditioning methods of heater surfaces on deposited TiN film thickness and uniformity, Microelectronic Engineering 75 (4) (2004) 405–412.
- [19] E. Hartmannsgruber, G. Zwicker, K. Beekmann, A selective CMP process for stacked low-k CVD oxide films, Microelectronic Engineering 50 (1–4) (2000) 53–58.
- [20] W.-C. Chen, S.-C. Lin, B.-T. Dai, M.-S. Tsai, Chemical mechanical polishing of low-dielectric-constant polymers: hydrogen silsesquioxane and methyl silsesquioxane, Journal of the Electrochemical Society 146 (8) (1999) 3004–3008.
- [21] T. Hara, T. Tomisawa, T. Kurosu, T.K. Doy, Chemical mechanical polishing of polyarylether low dielectric constant layers by manganese oxide slurry, Journal of the Electrochemical Society 146 (6) (1999) 2333–2336.

- [22] K. Mosig, T. Jacobs, K. Brennan, M. Rasco, J. Wolf, R. Augur, Integration challenges of porous ultra low-k spin-on dielectrics, Microelectronic Engineering 64 (1–4) (2002) 11–24.
- [23] Y.-J. Seo, W.-S. Lee, Chemical mechanical polishing of Ba_{0.6}Sr_{0.4}. TiO₃ film prepared by sol-gel method, Microelectronic Engineering 75 (2) (2004) 140–154.
- [24] F. Kuchenmeister, U. Schubert, C. Wenzel, SiLK dielectric planarization by chemical mechanical polishing, Microelectronic Engineering 50 (2000) 47–52.
- [25] T. Du, A. Vijayakumar, K.B. Sundaram, V. Desai, Chemical mechanical polishing of nickel for applications in MEMS devices, Microelectronic Engineering 75 (2) (2004) 234–241.
- [26] L. von Trotha, G. Mörsch, P. Wolters, G. Zwicker, Advanced MEMS Fabrication Using CMP, Semiconductor International, 2004. pp. 54–56.
- [27] Z.W. Zhong, S.C. Lim, A. Asundi, Effects of thermally induced optical fiber shifts in V-groove arrays for optical MEMS, Microelectronics Journal 36 (2) (2005) 109–113.
- [28] B. Ziaie, A. Baldi, M. Lei, Y. Gu, R.A. Siegel, Hard and soft micromachining for BioMEMS: review of techniques and examples of applications in microfluidics and drug delivery, Advanced Drug Delivery Reviews 56 (2004) 145–172.
- [29] S.R. Quake, A. Scherer, From micro- to nanofabrication with soft materials, Science 290 (2000) 1536–1540.
- [30] K.S. Ryu, X. Wang, K. Shaikh, D. Bullen, E. Goluch, J. Zou, C. Liu, Integrated microfluidic linking chip for scanning probe nanolithography, Applied Physics Letters 85 (2004) 136–138.
- [31] J. Engel, J. Chen, C. Liu, A polymer-based MEMS multi-modal sensory skin, Polymer Preprints 44 (2003) 534–535.
- [32] W. Sutomo, X. Wang, D. Bullen, S. Braden, C. Liu, Development of an end-point detector for parylene deposition process, Journal of Microelectromechanical Systems 12 (1) (2003) 64–69.
- [33] B.H. Weigl, R.L. Bardell, C.R. Cabrera, Lab-on-a-chip for drug development, Advanced Drug Delivery Reviews 55 (2003) 349–377.
- [34] H. Becker, L.E. Locascio, Polymer microfluidic devices, Talanta 56 (2002) 267–287.

- [35] Y. Zhao, F. Wang, Z.C. Cui, J. Zheng, H.M. Zhang, D.M. Zhang, S.Y. Liu, M.B. Yi, Study of reactive ion etching process to fabricate the PMMA-based polymer waveguide, Microelectronic Journal 35 (7) (2004) 605–608.
- [36] T.-H. Fang, W.-J. Chang, Nanoindentation characteristics on polycarbonate polymer film, Microelectronic Journal 35 (7) (2004) 595–599.
- [37] V.H. Nguyen, R. Daamen, R. Hoofman, Impact of different slurry and polishing pad choices on the planarization efficiency of a copper CMP process, Microelectronic Engineering 76 (1–4) (2004) 95–99.
- [38] Z.-C. Lin, C.-Y. Liu, Application of an adaptive neuro-fuzzy inference system for the optimal analysis of chemical-mechanical polishing process parameters, The International Journal of Advanced Manufacturing Technology 18 (1) (2001) 20–28.
- [39] N.G. Shankar, Z.W. Zhong, Defect detection on semiconductor wafer surfaces, Microelectronic Engineering 77/3–4 (2005) 337–346.
- [40] Y.-Y. Lin, S.-P. Lo, A study of a finite element model for the chemical mechanical polishing process, The International Journal of Advanced Manufacturing Technology 23 (9–10) (2004) 644–650.
- [41] Y.-Y. Lin, S.-P. Lo, A study on the stress and nonuniformity of the wafer surface for the chemical-mechanical polishing process, The International Journal of Advanced Manufacturing Technology 22 (5– 6) (2003) 401–409.
- [42] C.-Y. Ho, Z.-C. Lin, Analysis and application of Grey relation and ANOVA in chemical–mechanical polishing process parameters, The International Journal of Advanced Manufacturing Technology 21 (1) (2003) 10–14.
- [43] Z.W. Zhong, Y.G. Lu, 3D characterization of super-smooth surfaces of diamond turned OFHC copper mirrors, Materials and Manufacturing Processes 17 (2) (2002) 269–280.
- [44] Z.W. Zhong, Y.G. Lu, An AFM scanning moiré technique for the inspection of surface deformations, The International Journal of Advanced Manufacturing Technology 23 (5–6) (2004) 462–466.